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	Application No.	Applicant(s)	
Notice of Allowability	10/028,705	LEE ET AL.	
	Examiner	Art Unit	
	Zeev Kitov	2836	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.31	6 (OR REMAINS) CLOSED in the or other appropriate communication is substitution in the community of the comm	is application. If not inclu cation will be mailed in du	ided le course. THIS
1. X This communication is responsive to <u>06/21/04</u> .			
2. X The allowed claim(s) is/are <u>1 - 3, 5, 6, 8 - 11, 13</u> .			
3. 🔀 The drawings filed on 28 December 2001 are accepted by	the Examiner.		
4.			
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Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. T Notice of Infor	mal Patent Application (P	TO-152)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	. 6. 🔲 Interview Sum	mary (PTO-413),	. 3 102)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/	Paper No./Ma 08), 7. ☐ Examiner's An	il Date nendment/Comment	
Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's Sta	atement of Reasons for A	llowance
of Biological Material	9. Other		· · · · · · · · · · · · · · · · · · ·

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REASONS FOR ALLOWANCE

Examiner acknowledges a submission of the amendment and arguments filed on June 21,2004. Claims 4, 7, 12 and 14 - 18 are deleted; Claims 1 – 3, 5, 6 and 8 are amended. Amendment and arguments have overcome rejections under 103(a) and objection to the Drawings.

The following is an examiner's statement of reasons for allowance:

An amended independent Claim 1 discloses, inter alia, "p+ and n+ junctions of a first diode of the plurality of N diodes are connected to the input/output pad and a p+ junction of a second diode of the plurality of N diodes, respectively, the second diode through the N-1 diode of the plurality of N diodes are connected such that an n+ junction of each of the second diode through the N-1 diode is connected to the p+ junction of a subsequent diode, and an n+ junction of an N diode is connected to the substrate". The closest reference for the claim is Avery et al. in view of Miller and Menon et al. However, they do not disclose an "n+ junction of an N diode being connected to the substrate". It is essential for understanding of the claim language to emphasizes a difference

Another independent Claims 8 recites the same limitation.

Allowability resides, at least in part, in the above-described limitations, which has not been disclosed in the Prior Art in a search.

between the ground terminal connection and the substrate connection of the N diode.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Zeev Kitov whose telephone number is (571) 272-2052. The examiner can normally be reached on 8:00 – 4:30. If attempts to reach examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on (571) 272 – 2800, Ext. 36. The fax phone number for organization where this application or proceedings is assigned is (703) 872-9306 for all communications.

Z.K. 08/11/2004

> BRIÁN-SÍRCUS SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800